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LMMIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Marc Tremblay

Title: GROUPING LOGIC CIRCUIT IN A PIPELINED SUPERSCALAR  
PROCESSOR

Application No.: 09/583,097

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Examiner: : E. Chan

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Technology Center 2100

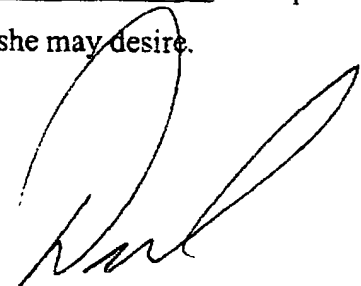
COMMISSIONER FOR PATENTS  
Washington, DC 20231

October 10, 2002

**POWER TO INSPECT**

Dear Sir:

Please permit Jennifer Harris-Lohse of Harris & Associates to inspect the above-entitled application, and to make copies of the papers that she may desire.



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